

International Symposium on Leading-Edge Holography Technologies

Organizer: Leading-Edge Holography Technologies Research and Development Center,
Research Institute for Science and Technology

Date October 30, 2015

Place Katsushika Campus library, 6-3-1 Nijuku, Katsushika-ku, Tokyo 125-8585, Japan

Program

9:00 Registration in the library hall of Katsushika Campus

10:00 Opening of the symposium, Manabu Yamamoto, Department of Applied Electronics, Tokyo University of Science

10:05 – 10:20 Outline of Leading-Edge Holography Technologies Research and Development Center and stronghold formation of next generation holographic technology, Manabu Yamamoto

10:20 – 11:00 James W. M. Chon, Faculty of Science, Engineering and Technology, Swinburne University of Technology, Plasmonic nanorod based multilayered optical data storage for archival storage (Invited talk)

11:00 – 11:20 Shuhei Yoshida, Department of Applied Electronics, Tokyo University of Science, Design and Evaluation of Diffractive Optical Element for Optical Storage

11:20 – 11:40 Noriyuki Unno, Department of Applied Electronics, Tokyo University of Science, High throughput 3D nanopatterning technique using a combination of electron beam lithography and nanoimprint,

11:40 – 12:00 Takahiko Okabe, Department of Mechanical Engineering, Tokyo University of Science, Rotational Electron Beam Machining System Using a Vacuum Compatible Hydrodynamic Spindle

12:00 – 13:30 Lunch time

13:30 – 13:50 Shin-ichi Satake, Department of Applied Electronics, Tokyo University of Science, Overview of Holography application measurement group

13:50 – 14:30 Takashi Kakue, Graduate School of Engineering, Chiba University, High-speed holographic imaging system based on single-shot phase-shifting interferometry (Invited talk)

14:30 – 14:50 Kazuhisa Yuki, Department of Mechanical Engineering, Tokyo University of Science, Yamaguchi, Three-dimensional flow visualization of a sphere-packed pipe flow by digital holographic PTV utilizing refractive index-matching method

14:50 – 15:10 Ichiro Matsuda, Department of Electrical Engineering, Tokyo University of Science, Title: Two-Stage Coding of Holographic Images Used for 3D Flow Field Measurement

15:10 – 15:30 Coffee break

15:30 – 17:00 Poster session

17:00 Closing remarks

17:20 - Symposium Banquet